



**COUNCIL OF
THE EUROPEAN UNION**

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NOTE

From: Permanent Representatives Committee (Part 1)
To: Council

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Subject: - Communication from the Commission on "High Performance Computing:
Europe's place in a Global Race"
- Adoption of Council conclusions

1. On 16 February 2012, the Commission transmitted to the Council its Communication titled "High Performance Computing: Europe's place in a Global Race".

The Communication highlights the strategic nature of High-Performance Computing (HPC) as a crucial asset for the EU's innovation capacity, and calls on Member States, industry and the scientific communities, in cooperation with the Commission, to step up joint efforts to ensure European leadership in the supply and use of HPC systems and services by 2020.

2. As a response to the Commission's Communication, the Presidency proposed draft Council conclusions which were examined by the Permanent Representatives Committee in its meetings on 24 May 2013.

3. A consensus on the text has been reached and the Council ("Competitiveness"), at its session on 29-30 May 2013, is therefore called upon to adopt the conclusions set out in annex to this document.
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**Draft Council Conclusions on
'High Performance Computing: Europe's place in a Global Race'**

THE COUNCIL OF THE EUROPEAN UNION

RECALLING

- the conclusions of the European Council of 11 and 12 December 2008¹, which called for the launching of a European plan for innovation, combined with the development of the ERA and with reflection on the future of the Lisbon Strategy beyond 2010;
- its conclusions of 29 May 2009² on Research Infrastructures and the regional dimension of the ERA which called on the Commission to pursue sustainability, global connectivity, interoperability and unimpeded use of pan-European e-Infrastructures, and on the Member States to consider the role of e-Infrastructures in their national roadmaps and/or programmes for research infrastructures;
- its conclusions of 3 December 2009³ on the Future of ICT research, innovation and infrastructures which invited the Member States to have more coordinated investments in High Performance Computing (HPC) and the Commission to propose financial incentives for jointly developing and sharing research infrastructures in exascale computing, and on Member States and the Commission to pool their investments in HPC under PRACE⁴ and to strengthen the position of European industry and academia in the use, development and manufacturing of advanced computing products, services and technologies;
- its conclusions of 17 May 2010⁵ on various issues related to the development of the ERA, urging the need for further development of computing infrastructures, such as PRACE ;

¹ Doc. 17271/1/08

² Doc. 10612/09.

³ Doc. 17190/09.

⁴ PRACE (Partnership for Advanced Computing in Europe) is an ESFRI project creating a persistent pan-European Research Infrastructure for High Performance Computing: www.prace-ri.eu

⁵ Doc. 9451/10.

1. WELCOMES the attention brought to the issue of High Performance Computing and TAKES NOTE OF the great emphasis placed by the Commission on the strategic nature of HPC for the EU in its communication entitled "'High Performance Computing: Europe's place in a Global Race"⁶, advocating a renewed European strategy in HPC and proposing an action plan to position the EU as a centre of innovation, a hub of scientific excellence and a global partner;
2. HIGHLIGHTS that HPC is an important asset for the EU's innovation capacity and STRESSES its strategic importance to the EU's industrial and scientific capabilities as well as its citizens, by supporting the development of innovative industrial products and services, increasing competitiveness, and addressing societal and scientific grand challenges more effectively; NOTES that other world nations have declared HPC an area of high importance and massively increased their efforts in this area;
3. EMPHASISES the importance of deploying and maintaining a world-class and sustainable European HPC infrastructure, and RECOGNISES the achievements of PRACE⁷ in pooling leadership-class computing systems and making them available to all researchers in the EU and associated countries, on the basis of, and in order to enhance, scientific excellence and innovation and the need to maintain this approach;
4. EMPHASIZES the strength of the EU in applications, low-power computing and integration and HIGHLIGHTS that Europe has the technology, knowledge and human skills to develop capabilities covering the whole technological spectrum of the next generation of HPC systems including software, services and applications (exascale computing); Stressing the importance of developing state-of-the-art HPC technologies, systems, software, applications and services in Europe AGREES that maintaining and extending European strengths in these areas would support growth, sustainability (green ICT) and competitiveness in science, the ICT industry and the economy in general.

⁶ Doc. 6596/12

⁷ Partnership for Advanced Computing in Europe (PRACE) <http://www.prace-ri.eu/>

5. ACKNOWLEDGES the efforts of HPC stakeholders to support the implementation of an EU-level HPC strategy, in particular PRACE - as an independent actor on the scientific side - and the industry-led European Technology Platform for HPC (ETP4HPC);
6. STRESSES the importance of supporting and strengthening the dual role of European industry in HPC, both as supplier of state-of-the-art technologies and systems, and as user of HPC to innovate in products, processes and services;
7. STRESSES the importance of supporting and strengthening the role of academic HPC users.

UNDERLINING the overall objective to achieve European leadership in the development and use of HPC systems, software, applications and services by 2020:

8. RECOGNISES the need for an EU-level policy in HPC addressing the entire HPC ecosystem: world-class and sustainable HPC infrastructure; HPC use by Science and by industry, including SMEs; HPC industrial supply for development of exascale computing; excellence in HPC software, methodology and applications; and achieving a level-playing field for EU companies;
9. RECOGNISES the need for all relevant actors, public and private, to work in partnership; INVITES Member States, the Commission, and industry to ensure appropriate investments in HPC (noting the importance of investment in software development, HPC methodologies, education and training, in addition to hardware) and to optimise European investment while taking into account past efforts of Member States for investments in world-class HPC infrastructure; INVITES Member States and the Commission to exchange and to share priorities and plans for HPC development in the context of the appropriate fora, in association with PRACE and the European Technology Platform on HPC;

10. NOTES the importance of Centres of Excellence and networks in HPC applications addressing key societal, scientific and industrial challenges in areas that are strategic for Europe; in this context underlines the role of training for users;
11. INVITES Member States to consider setting up HPC Competence Centres to facilitate the development of required competences in HPC, to facilitate access to HPC capabilities and services to science and to support the transfer of relevant expertise from supercomputing centres to industry – including to SMEs.
12. INVITES Member States and the Commission to step up the efforts to substantially increase the supply of scientists and engineers with HPC skills, through adequate training and educational programmes addressed to industry and academia;
13. ASKS the Commission to explore funding possibilities and instruments to support the development of leadership-class HPC capabilities in Europe as well as the acquisition of world-class HPC systems on the global market on the basis of open competition to address the needs of various HPC user communities;
14. INVITES the Commission to continue its efforts to reduce inequalities in the access of EU manufacturers to foreign HPC markets with the relevant countries, in order to promote fair access by European companies and a level playing field, which also includes fair access for HPC-purchasers to non-European HPC-systems;
15. INVITES the Commission to develop and elaborate its plans for HPC and to explore all possible support for academic and industrial research and innovation under Horizon 2020 - covering, inter alia, design of HPC hardware, components and software as well as HPC infrastructure, services and applications - and to present its plans through the appropriate governance channels before the end of 2013; INVITES the Commission to report to the Council on progress made in these fields before the end of 2015.